REMARKS

Claims 1 and 2 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections and objections are respectfully requested in light of the above amendments and following remarks.

The Examiner alleges that a proposed drawing correction for Figures 1-3 is required.

By separate letter, a proposed drawing has been submitted of Figures 1-3.

It is respectfully submitted that Figures 1-3 are in full compliance with MPEP § 608.02(g).

The drawings were objected to as failing to comply with 37 CFR 1.84.

A proposed drawing correction has been submitted by separate letter.

The drawings were objected to under 37 CFR 1.83.

By separate letter, a proposed drawing correction has been submitted.

It is respectfully submitted that the drawings are in full compliance with 37 CFR 1.84 and 37 CFR 1.83.

The Examiner requires a new title.

By the instant amendment, the original title has been canceled and a new title has been provided that is clearly indicative of the invention to which the claims are directed.

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Claims 1-2 were rejected under 35 U.S.C. §102 as being anticipated by Hancock.

It is respectfully submitted that Hancock does not disclose or suggest the presently claimed invention including the track and hold circuit to input the clock signal. The output of comparators U2 and U3 are not input to the element 110.

It is respectfully submitted that Claims 1-2 patentably define over the applied art.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

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